

# Thermal contact resistance measurements on metal-semiconductor structures by scanning thermal microscopy and $3\omega$ method

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Reduced size of current electronic devices involves boundaries that play a crucial role in heat transfer inside such components. In particular, metal-semiconductor interfaces are widely present. Understanding the mechanisms of thermal transport undergone by energy carriers at the junction regions requires the determination of both thermal conductivities of materials in contact and thermal contact resistance between them [1].

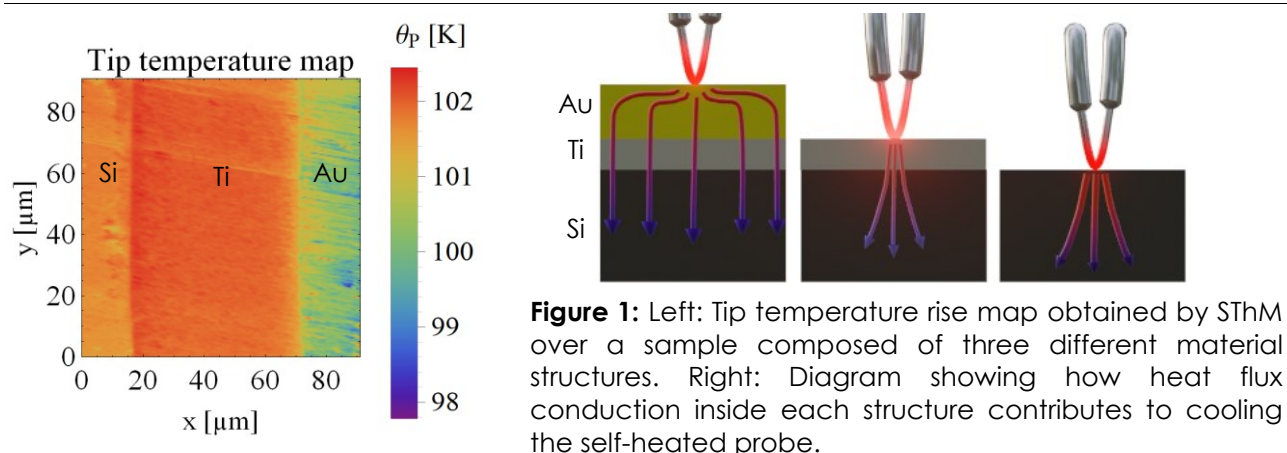
We study experimentally metal-semiconductor structures by means of scanning thermal microscopy (S<sub>Th</sub>M) and  $3\omega$  method. S<sub>Th</sub>M is based on a self-heated metallic thermal sensor that is part of a movable AFM tip and enters in contact with the sample surface [2]. This technique allows to perform thermal imaging with a spatial resolution down to the nanometric scale, at sample temperature close to ambient. The  $3\omega$  method is based on the deposition of a metallic resistive wire on top of the sample and whose electrical resistance is measured as a function of an alternating power input [3]. In contrast to S<sub>Th</sub>M, which is hardly able to be performed as a function of sample temperature, the  $3\omega$  method can provide thermal data over a wide temperature range through the use of a cryogenic system, while it does not allow for spatially-localised measurements.

Figure 1 shows an example of S<sub>Th</sub>M image over a sample containing a silicon substrate on top of which two metallic nanolayers were successively deposited. In order to estimate the thermal boundary resistances involved in the sample, the obtained data are compared to FEM simulations. In addition, in order to better know the semiconductor thermal properties, we perform thermal conductivity measurements of thin layers deposited over silicon substrates by means of the  $3\omega$  method. In this case, a sensitivity analysis is detailed and the experimental results are compared to a semi-analytical model [4].

## References

- [1] G. Hamaoui et al., Scientific Reports 8, 11352 (2018)
- [2] S. Gomès, A. Assy, P. -O. Chapuis, Physica Status Solidi 212, 3 (2015)
- [3] W. Jaber and P. -O. Chapuis, AIP Advances 8, 045111 (2018)
- [4] T. Borca-Tasciuc, A. R. Kumar and G. Chen, Rev. Sci. Instrum. 72, 2139 (2001)

## Figures



**Figure 1:** Left: Tip temperature rise map obtained by S<sub>Th</sub>M over a sample composed of three different material structures. Right: Diagram showing how heat flux conduction inside each structure contributes to cooling the self-heated probe.

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