

Performance of Surface Codes in realistic quantum hardware

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Abstract

One of the main obstacles for the construction of a many qubit quantum computer is quantum decoherence. Qubits within quantum computers tend to fail and prevent us from achieving their true potential. In order to tackle quantum decoherence, Quantum Error Correction Codes (QECC) aim to detect and correct those errors. Surface codes are a class of QECC which enjoy a great popularity in the current scientific community since they do not need a large number of qubits [1]. Most of the studies on the performance of surface codes assume all qubits experience noise following an independent and identical error probability distribution (i.i.d.). In this poster, supported by experimental

evidence in which individual qubits within the code present different decoherence times, we propose the independent non-identical error probability error model (i.n.i.d.) [2-4]. Based on such model, we observe the change in the performance of the planar code and we try to design an algorithm which can exploit the difference in error probability of the constituent qubits in order to enhance the aforementioned performance.

References

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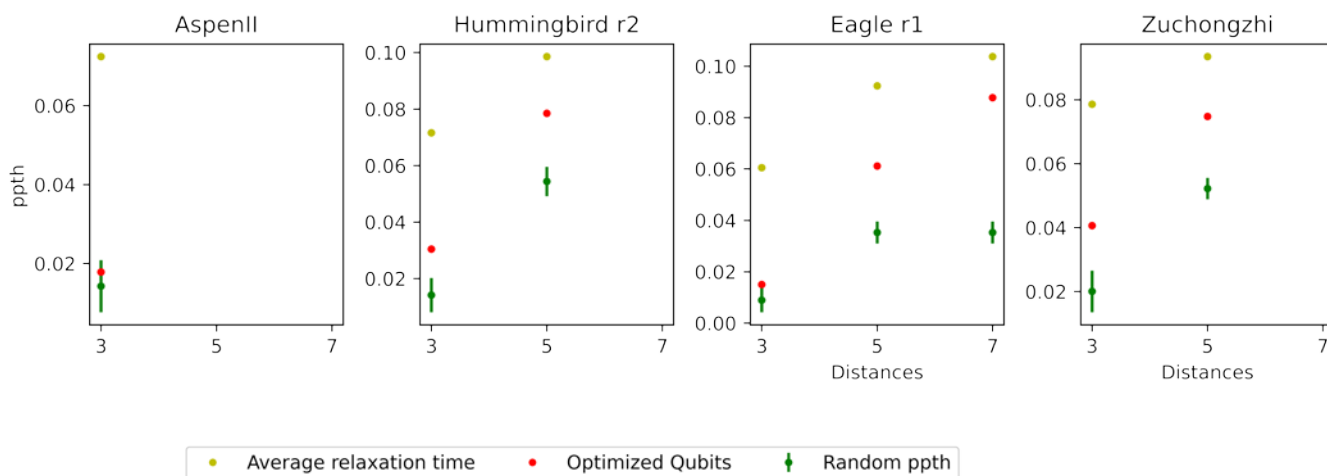


Figure 1: Pseudo-threshold for the random (i.n.i.d.), optimized (i.n.i.d.) and i.i.d. scenarios for 4 different quantum processors.