# Silicon Nanowires with Quantum Dots for experimentation in Quantum Technologies

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## Abstract

Silicon Nanowires (SiNWs) are one of the building blocks for the development of semiconductor spin qubits based on quantum dots. We are developing a platform based on novel nanofabrication approaches following two mix and match routes: i) optical and electron beam lithography (EBL); ii) EBL and focused ion beam (FIB) implantation. The platform is designed for allowing rapid experimentation of novel device concepts.

The first mix-and-match approach consists of 5 lithographic steps combining optical lithography and EBL using the same negative resist. The process allows to define more than 5000 individually connected SiNW devices (figure 1).

The second approach is based on a threestep fabrication scheme [1,2] using FIB implantation as a resistless lithography process followed by wet chemical etching. It allows to obtain suspended and not suspended silicon nanowires of sub-20 nm width. By the development of an annealing step, it is demonstrated the recovery of the electrical conduction up to four orders of magnitude, the out-diffusion of gallium and the promotion of nanocrystals (sub-10nm) embedded and constricted inside the silicon nanostructures [3,4]. The nanocrystalline nature of the resulting SiNWs allowed the development of single hole transistors (SHoTs)] as depicted in figure 2.

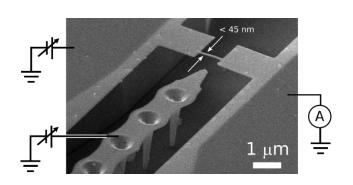
## References

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## Figures



**Figure 1:** Photograph of a 100 mm wafer containing more than 5,000 connected silicon nanowires



**Figure 2:** SEM image of a single hole transistor fabricated by a three step rapid prototyping process based on focused ion beam [4]