

# Nickel silicide phase change transformation upon nanosecond laser annealing

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In recent devices, nickel silicide is widely used due to its low resistivity and silicon consumption [1]. A classical process flow used for nickel silicide fabrication consists in a first anneal to form the Ni<sub>2</sub>Si phase, a selective etch to remove unreacted metal, and a second anneal to obtain the low resistivity NiSi phase [1]. However, the high diffusivity and the low temperature stability of the phase of interest is a problem [2]. Research has been performed on the second anneal to overcome these problems. “Plateau” anneal [3], spike anneal [4] and then millisecond anneal have been investigated with significant gains in agglomeration robustness [5–7]. Nanosecond anneal is a logical alternative but still largely unexplored when the energy is kept below the melting threshold [8].

Sample preparation is described in **Figure 1**. On a p-doped bulk silicon substrate, a partial silicidation followed by a selective etch is performed. Then, nanosecond (ns) anneals at different energy densities are applied. The sheet resistance (R<sub>s</sub>) value as a function of nanosecond laser energy density is shown, for 10 laser shots in **Figure 2**. The curve shows four distinct regimes. R<sub>s</sub> exhibits a high resistance plateau, followed by an intermediate one, then a low resistance plateau. The last regime starts with an abrupt R<sub>s</sub> increase due to the onset of silicide melting.

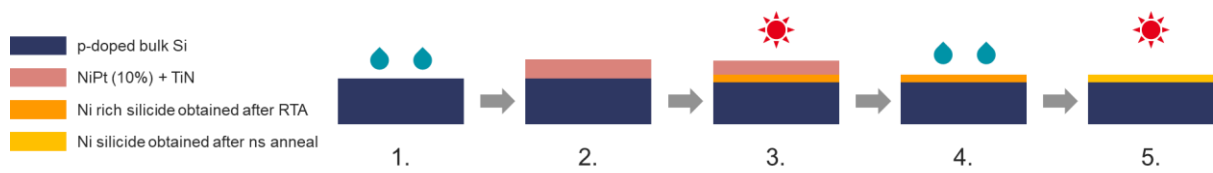
To better understand this phase transformation curve, in-plane and out-of-plane X-ray diffraction (XRD) measurements were performed (**Figure 3**). In the diagrams corresponding to the initial plateau and the melt region at around 1 J/cm<sup>2</sup>, small peaks of Ni<sub>3</sub>Si<sub>2</sub> (identified by circles in the figure) are detected. In the intermediate plateau, the main phase observed is Ni<sub>3</sub>Si<sub>2</sub>. In the low resistivity plateau, the NiSi phase is identified (triangles) with residual peaks of Ni<sub>3</sub>Si<sub>2</sub> but with a different preferential orientation compared to that in the intermediate plateau. In melt region at high ED no other peaks than the one due to the substrate is obtained. The formed silicide(s) may be amorphous or made of too small nanocrystallites to be detected.

The possibility of obtaining a low resistivity NiSi phase by nanosecond laser anneal was thus demonstrated. The phase sequence obtained with ns anneal was atypical. In fact, the expected Ni<sub>2</sub>Si phase was not seen in XRD for the initial plateau. Furthermore, an intermediate phase between the first Ni silicide phase and the NiSi phase was shown to be Ni<sub>3</sub>Si<sub>2</sub>. Finally, the melt regime showed a huge variation in resistivity, which was not correlated with the evolution of the diffraction peaks, since no peaks other than those due to the substrate were detected in XRD diagrams at high energy density.

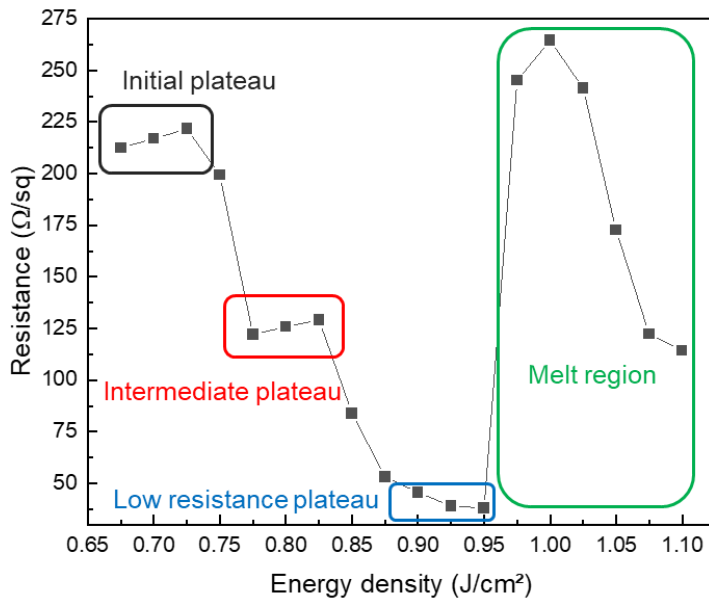
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1. J. Foggiato et al., *Mat. Sci. and Eng.: B* **114–115**, 56 (2004).
2. N. Breil et al., *Microelec. Eng.* **137**, 79 (2015).
3. B. Imbert et al., *Microelec. Eng.* **87**, 245 (2010).
4. T. Futase et al., *IEEE Transac. on Semi. Manuf.* **22**, 475 (2009).
5. C. Ortolland et al., in *2009 IEEE International Electron Devices Meeting (IEDM)* (2009), pp. 1–4.
6. M. Gregoire et al., in *2011 IEEE International Interconnect Technology Conference* (2011), pp. 1–3.
7. J. Kim et al., *IEEE Trans. on Elec. Dev.* **66**, 389 (2019).
8. J. Kim et al., *Journal of Alloys and Compounds* **788**, 1013 (2019).

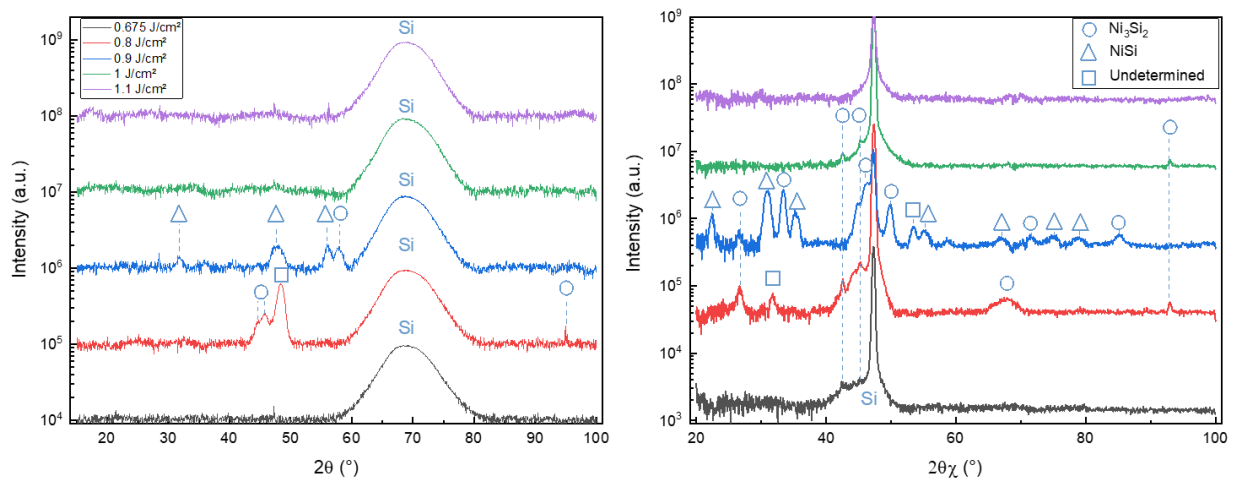
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**Figure 1: Details of the sample preparation flow. 1. Wet surface preparation. 2. 16 nm NiPt (10%) deposition. 3. Rapid thermal annealing (RTA) for partial silicidation. 4. Selective etch to remove unreacted metal. 5. Nanosecond laser anneal to complete phase transformation.**



**Figure 2: Sheet resistance ( $R_s$ ) as a function of nanosecond laser energy density for 10 laser shots.**



**Figure 3: Out-of-plane (left) and in-plane (right) X-ray diffraction diagrams for different nanosecond laser energy densities.**