

Limitations and opportunities of metal-organic chemical vapor deposition of MoS₂ and WS₂

Benjamin Groven

Yuanyuan Shi, Henry Medina Silva, Vladislav Voronenkov¹, Dries Vranckx, Raf Rennen, Wim Huysecom, Jarne De Boeck, Brecht De Vos, Sebastiaan Nijs, Sreetama Banerjee, Quentin Smets, Tom Schram, Xiangyu Wu, Dennis Lin, Inge Asselberghs, Stefan De Gendt¹, Pierre Morin
imec, Kapeldreef 75, Leuven, Belgium; ¹KU Leuven, Celestijnenlaan 200F, Leuven, Belgium
Benjamin.Groven@imec.be

Group-VI transition metal dichalcogenides (MX₂), such as molybdenum- and tungsten disulfide (MoS₂, WS₂), emerge as two-dimensional (2D) semiconductors that can complement workhorse silicon as channel material in ultra-scaled nanoelectronic devices [1,2]. Manufacturable approaches to deposit highly crystalline MX₂ layers, tailor the layer number down to the atomic level, and remain compatible with temperature sensitive structures, are essential to unlock the desired material functionality. However, fundamental understanding is lacking on how to design Fab-compatible chemical deposition processes for 2D MX₂, such as chemical vapor deposition (CVD). This presentation focuses on metal-organic (MO-)CVD from metal hexacarbonyl and dihydrogen sulfide precursors using industry-standard, customized 200 mm and 300 mm epitaxial reactors. Based on a qualitative growth model, we review the limitations and opportunities of MX₂ MOCVD, such as poor diffusional transport of adsorbed surface species and undesired metal co-deposition (Figure 1). We describe how the MX₂ growth behavior depends on the starting surface by comparing the nucleation and growth evolution on an amorphous (e.g., SiO₂) and single crystalline (e.g., sapphire) substrate using complementary microscopy and spectrometry techniques [3]. From that insight, we reveal how these limitations can be overcome, yielding microcrystalline MoS₂ monolayers on sapphire with median mobilities at 30 cm²/Vs and drive currents up to 420 μ A/ μ m [4].

References

- [1] Q. Smets et al., IEDM, 23.2. 1-23.2. 4 (2019)
- [2] D. Lin et al., Symposium on VLSI Technology, 1-2 (2021)
- [3] Y. Shi et al., ACS Nano, 15 (2021) 9482-9494
- [4] Wu et al., IEDM, 7.4.1-7.4.4 (2021)

Figures

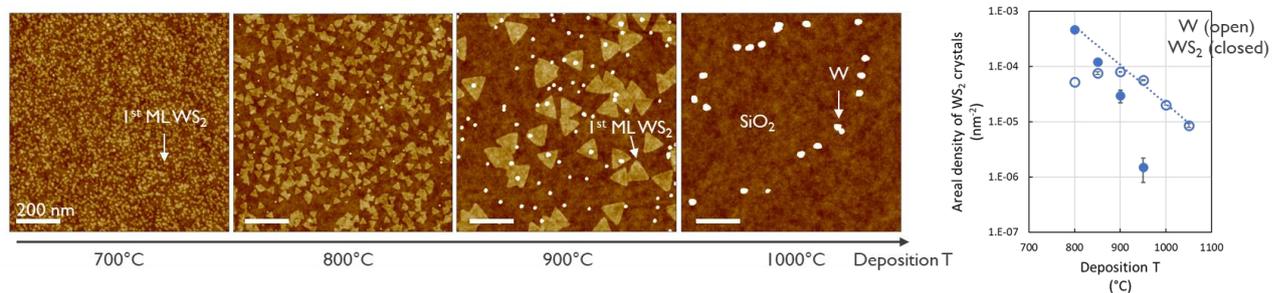


Figure 1: Growth evolution of WS₂ MOCVD on amorphous SiO₂ starting surface as a function of deposition temperature. W and WS₂ are simultaneously deposited. The areal density of WS₂ crystals decreases with deposition temperature but remains high at technologically relevant deposition temperatures introducing grain boundaries when neighbouring crystals coalesce.