

Two-dimensional van der Waals Heterostructures for Device Applications

Graphene has brought a great deal of excitement to nanoscience community with its attractive and unique properties. Such excellent characteristics have triggered highly active researches on other 2D materials. These emerging 2D materials are promising candidates for flexible and transparent electronics. Furthermore, new physics observed in 2D materials allow for development of new-concept devices by using their valleys, tunneling effect, photoluminescence, and optical responsivity. Recently, vdWHs have been achieved by putting these 2D materials onto another, in the similar way to build Lego blocks. This enables us to investigate intrinsic physical properties of atomically-sharp heterostructure interfaces and fabricate high performance optoelectronic devices for advanced applications. In this talk, I will briefly introduce critical issues to be resolved for practical applications of 2D materials, including contact resistance, environmental sensitivity, coupling at interfaces, and surface modification.[1-3] Recently, we have shown how to solve these problems to improve the device performance of 2D materials. Further, we demonstrated high performance electronic devices consisting of vdWHs, such as transistors, memories, strain sensors, and solar cells.[4-8] Our approaches would allow us to come closer to practical applications of 2D materials in industry.

References

- [1] J. Kwon, et al. "Thickness-Dependent Contact Resistance of MoS₂ Field-Effect Transistors" *Nanoscale* Just accepted, 2017.
- [2] Y. J. Yu, et al. "Epitaxially Self-Assembled Alkane Layers for Graphene Electronics" *Advanced Materials* 29, 1603925, 2017.
- [3] S. Kim, et al. "In Situ Thickness Control of Black Phosphorus Field-Effect Transistors via Ozone Treatment" *Nano Research* 9, 3056-3065, 2016.
- [4] G. H. Lee, et al. "Highly Stable, Dual-Gated MoS₂ Transistors Encapsulated by Hexagonal Boron Nitride with Gate-Controllable Contact Resistance and Threshold Voltage" *ACS Nano* 9, 7019–7026, 2015.
- [5] X. Cui, et al. "Multi-Terminal Transport Measurements of MoS₂ Using van der Waals Heterostructure Device Platform" *Nature Nanotechnology* 10, 534–540, 2015.
- [6] C. H. Lee, et al. "Atomically Thin p-n Junctions with van der Waals Heterointerfaces" *Nature Nanotechnology* 9, 676-681, 2014.
- [7] G. H. Lee, et al. "Flexible and Transparent MoS₂ Field-Effect Transistors on Hexagonal Boron Nitride-Graphene Heterostructures" *ACS Nano* 7, 7931–7936, 2013.
- [8] M. S. Choi, et al. "Controlled Charge Trapping by MoS₂ and Graphene in Ultrathin Heterostructured Memory Devices" *Nature Communications* 4, 1624 (2013)

Figures

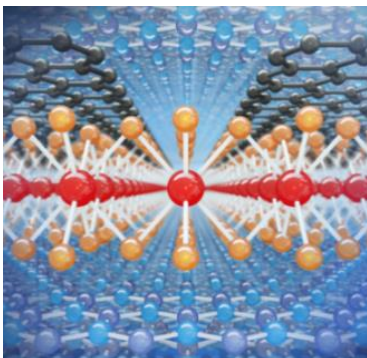


Figure 1: Schematic of vdWHs