A single hole spin with enhanced coherence in natural silicon

Presenting Author : B.Brun

Co-Authors : N. Piot^{1*}, B. Brun^{1*}, V. Schmitt¹, S. Zihlmann¹, V. P. Michal², A. Apra¹, J. C. Abadillo-Uriel², X. Jehl¹, B. Bertrand³, H. Niebojewski³, L. Hutin³, M. Vinet³, M. Urdampilleta⁴, T. Meunier⁴, Y.-M. Niquet², R. Maurand¹, S. De Franceschi¹

1-Univ. Grenoble Alpes, CEA, Grenoble INP, IRIG-Pheliqs, Grenoble, France. 2-Univ. Grenoble Alpes, CEA, IRIG-MEM-L Sim, Grenoble, France. 3-Univ. Grenoble Alpes, CEA, LETI, Minatec Campus, Grenoble, France. 4-Univ. Grenoble Alpes, CNRS, Grenoble INP, Institut Néel, Grenoble, France.

CEA Grenoble, 17 Av des martyrs, 38000 Grenoble

boris.brun-barriere@cea.fr

Spin quantum bits in semiconductor appear to be one of the most promising technologies for the development of the quantum processor. Although electrons are the focus of modern research, holes exhibit many interesting physical properties. In particular, spin-orbit interaction allows a fully electrical control of the hole spin qubit state [1]. As a drawback, any external electric perturbation is likely to induce decoherence. Here we demonstrate for a single hole qubit in silicon the existence of sweet spots at which the qubit is decoupled from charge noise while keeping an efficient electrical driving. We first realize spin singleshot readout of the first hole accumulated in a natural silicon quantum dot made from a semi-industrial 300mm CMOS foundry. Subsequently, we characterize the hole spin g-tensor and its susceptibility to electric fields by coherent manipulation depending on the external magnetic field orientation. It reveals optimal operation points at which the longitudinal spin-electric susceptibility is minimal. At these sweet spots, we measure a Hahn-Echo decay time in the order of 100us while maintaining Rabi frequencies in the MHz range. This work opens new perspectives for quantum processing based on spin-orbit qubits.

(arXiv:2201.08637v1)

References

Figures

- [1] Bosco et al, PRX Quantum, 2021
- [2] Elzermann et al, Nature, 2004



Figure 1: Simplified 3-dimensional representation of a silicon (yellow)-on-insulator (green) nanowire device with four gates. Gate 2 (G2) defines a quantum dot hosting a single hole. The drain contact is connected to an off-chip, surface-mount inductor to enable rfreflectometry readout.



Figure 2: Normalized echo amplitude versus the free evolution time using a Hahn echo sequence (See top left inset). Black dashed line represents the Gaussian fit to extract the extended coherence time at the optimised working point.

QUANTUMatter2022