

## **Engineering of Charge Current Flow in Nanoporous Graphenes**

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During the last decade, on-surface fabricated graphene nanoribbons (GNRs) have gathered enormous attention due to their semiconducting  $\pi$ -conjugated nature and atomically precise structure.<sup>[1]</sup> GNRs are regularly characterized by means of scanning probe microscopy (SPM), which has also allowed to study exotic electronic quantum phases realized in these nanostructured materials.<sup>[2]</sup> A significant breakthrough in the same field was the recent fabrication at ICN2 of nanoporous graphene (NPG) as a 2D array of laterally bonded GNRs.<sup>[3]</sup> This covalent integration of GNRs could enable complex electronic functionality at the nanoscale, with the ability to tune the electronic coupling between GNRs within NPGs. In this talk, I will present our recent works, based on quantum chemical calculations and large-scale transport simulations, and in which we demonstrate unprecedented electronic control of NPG either through a rational chemical design<sup>[4]</sup> or by external means such as electrostatic gates (Fig. 1a).<sup>[5]</sup> Our simulations of local injection of currents in NPGs evidence the control capability of spatial current distribution with subnanometer precision (Fig. 1b-c), results which could be experimentally probed using SPM. Our most recent studies also generalize these ideas to other types of carbon nanostructures<sup>[6]</sup> and, importantly, their applicability at finite temperature. A fundamental strategy to design carbon nanodevices with built-in externally tunable electronics and spintronics is thus proposed, and should be key for future applications such as bio-chemical nanosensing and carbon nanoelectronics.

## References

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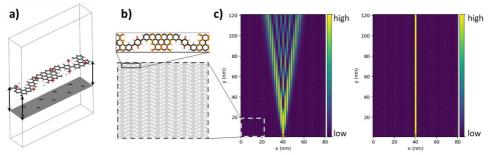


Figure 1: (a) Periodic unit cell of an electrostatically gated (gray plane) newly designed NPG, where GNRs are connected via aryl-quinone units. (b) Construction of large-scale NPG devices. (c) Spreading of currents (maximum magnitude in yellow) locally injected at the bottom of large-scale NPG devices (see red dot) at different applied gates (as outlined in a).