GrapheneforUS

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2D Materials for Industry

Abstract

Semiconductor sales will reach over \$500 billion worldwide in 2021, a gigantic industry that keeps on growing with increasing demand for faster, more powerful, and smaller chips. However, as we keep scaling, the silicon (Si) transistor will soon reach its physical limit, and there is a pressing need to find an alternative post-Si material to enable the continuation of Moore's Law. In the early 2000s, scientists discovered that graphite could be exfoliated down into an atomic form, going from a 3D bulk material down to a 2D stable honeycomb lattice of carbon atoms called graphene. Scientists marveled at graphene's astonishing electrical and mechanical properties, however, for all that graphene has to offer, it lacks a band gap that is essential for logic devices. This created a surge in research on materials beyond graphene, scientists searching for an elusive 2D material that would possess a bandgap to satisfy the need of the semiconducting industry. Monolayer Transition Metal Dichalcogenides (TMDs) possess the bandgap that graphene lacks, and with the vast variety of TMDs available, coupled with its encouraging electrical properties, make TMDs a promising candidate.

In this talk, I will present some of Intel's published research on 2D materials focusing on TMDs, from synthesis and characterization to innovative applications. I will demonstrate, that in Components Research at Intel, we are continuously looking at ways to improve future technologies, and enable the continuation of Moore's Law.

References

- R. Chau, "Process and Packaging Innovations for Moore's Law Continuation and Beyond," 2019 IEEE International Electron Devices Meeting (IEDM), 2019, pp. 1.1.1-1.1.6, doi: 10.1109/IEDM19573.2019.8993462.
- [2] C. Dorow *et al.*, "Advancing Monolayer 2-D nMOS and pMOS Transistor Integration From Growth to Van Der Waals Interface Engineering for Ultimate CMOS Scaling," in *IEEE Transactions on Electron Devices*, vol. 68, no. 12, pp. 6592-6598, Dec. 2021, doi: 10.1109/TED.2021.3118659.
- [3] S. King et al., "A Selectively Colorful yet Chilly Perspective on the ^{Highs} and Lows of Dielectric Materials for CMOS Nanoelectronics," 2020 IEEE International Electron Devices Meeting (IEDM), 2020, pp. 40.1.1-40.1.4, doi: 10.1109/IEDM13553.2020.9371942.
- [4] K. P. O'Brien et al., "Advancing 2D Monolayer CMOS Through Contact, Channel and Interface Engineering," 2021 IEEE International Electron Devices Meeting (IEDM), 2021, pp. 7.1.1-7.1.4.