

Archana Venugopal¹

Arup Polley¹, Luigi Colombo², Arul Vigneswar Ravichandran², Jiyoung Kim²,
Jaebeom Lee³, Antonio Lucero³, Lanxia Cheng³, Joy Lee³, Varun Kumar³

¹Texas Instruments,

²University of Texas at Dallas

³Previously at UTDallas

a.venugopal@ti.com

Graphene and the Analog Landscape

Graphene has been around since 2004, yet the path to integration with standard semiconductor flow stays elusive. This talk will cover the potential for graphene in a predominantly analog company like TI, attempts towards integration and issues faced in the process.