

# Interface-Controlled Strain Engineering in MoS<sub>2</sub> Devices via ALD Dielectric Encapsulation for Enhanced Carrier Mobility

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## Abstract

Two-dimensional transition metal dichalcogenides (TMDCs), such as MoS<sub>2</sub>, are promising candidates for next-generation electronics. However, limited carrier mobility remains a critical bottleneck limiting device performance.<sup>[1]</sup> While strain engineering is an effective approach to modulate the electronic properties of TMDCs, conventional methods typically rely on external mechanical deformation, which is difficult to integrate into scalable device processes.<sup>[1]</sup> In this work, we investigate a scalable, process-induced strain engineering strategy for MoS<sub>2</sub> based on atomic layer deposition (ALD) dielectric encapsulation. The in-plane E<sub>2g</sub> Raman mode exhibits a systematic redshift with increasing top dielectric thickness, consistent with process-induced tensile strain in the MoS<sub>2</sub> channel.<sup>[1]</sup> However, the overall strain transfer efficiency is significantly limited by interfacial slip between the MoS<sub>2</sub> and the conventional SiO<sub>2</sub> substrate.<sup>[2,3]</sup> To overcome this limitation, we introduce ALD-grown HfO<sub>2</sub> bottom dielectric layer. Photoluminescence and Raman spectroscopy reveal that the bottom dielectric layer significantly improves strain transfer to the MoS<sub>2</sub> channel. This enhancement is attributed to improved interface-mediated strain transfer enabled by the engineered bottom dielectric interface and top dielectric encapsulation.<sup>[2,3]</sup> By employing this dual-sided dielectric encapsulation structure, the interfacial slip is effectively suppressed, and the process-induced tensile strain in MoS<sub>2</sub> is maximized. Electrical characterization further shows improved carrier mobility, suggesting that interface-controlled strain engineering provides a practical route for optimizing 2D semiconductor devices without relying on complex external strain apparatus.<sup>[1]</sup>

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## References

- [1] Y. Zhang et al., ACS Nano 18 (2024) 12377–12385.
- [2] V. L. Nguyen et al., Nature Electronics 6 (2023) 146–153.
- [3] Y. Sun, R. Wang, and K. Liu, Applied Physics Reviews 4(1) (2017) 011301.

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## Figures

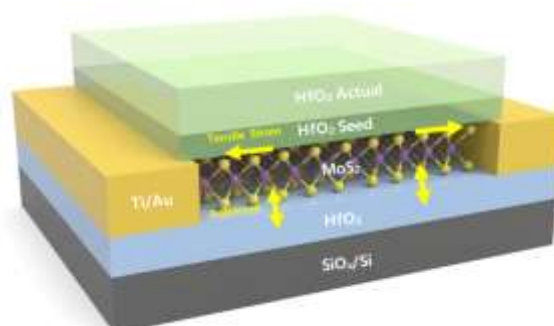


Figure 1: Schematic view of the MoS<sub>2</sub> device with dual-sided HfO<sub>2</sub> encapsulation.