

# Ink-jet Printed Graphene-Silicon Schottky Diodes

Dr. Alessandro Grillo<sup>1</sup>

Zixing Peng<sup>1</sup>, Aniello Pelella<sup>2</sup>, Antonio Di Bartolomeo<sup>2</sup>, Cinzia Casiraghi<sup>1</sup>

<sup>1</sup>Department of Chemistry, University of Manchester, Manchester, M13 9PL, UK

<sup>2</sup>Physics Department "E. R. Caianiello", University of Salerno, Fisciano 84084, Salerno, Italy

[alessandro.grillo@manchester.ac.uk](mailto:alessandro.grillo@manchester.ac.uk)

## Abstract

Integration of graphene (Gr) in silicon-based technology is of crucial importance for enabling the next generation electronics, photonics and sensors [1]. Although numerous works have reported devices based on Gr-Si junctions, the integration process relies on the use of high quality Gr produced by Chemical Vapour Deposition (CVD), making the fabrication steps expensive, time consuming and by limiting the large scale devices' reproducibility.

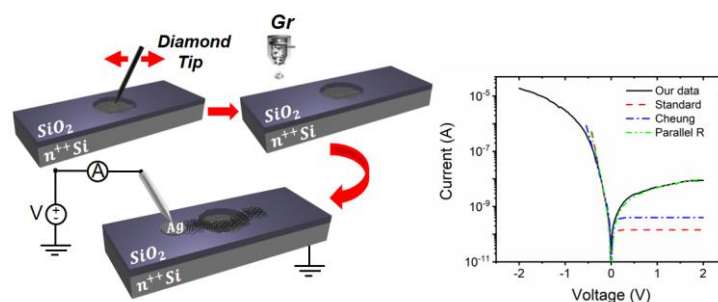
In this work we show that inkjet-printing enables simple and scalable integration of Gr into Si-technology [2]. We developed a simple fabrication procedure, based on the mechanical or chemical etching of the SiO<sub>2</sub> layer from a standard Si wafer, followed by the inkjet printing of water-based printable Gr inks [3] on the exposed area, leading to Schottky diodes with excellent rectifying behaviour and figures of merit, comparable to those produced with CVD graphene. We fully characterized the devices and applied several theoretical models achieving deep understanding of the underlying physics of the devices. We also investigated the optical response of the diodes by demonstrating a spatially selective photodetector.

Our results demonstrate that inkjet printing is a cost-effective and scalable method, which is also compatible with back-end-of-line fabrication processes for the integration of graphene in the modern Si-technology.

## References

- [1] Akinwande et al, *Nature*, 573, 507–518 (2019)
- [2] Grillo et al, *ACS Nano*, 17, 2, 1533–1540 (2023)
- [3] McManus et al, *Nature Nano*, 12, 343 (2017)

## Figures



**Figure 1:** Schematic of the fabrication process and I-V characteristic of the printed Gr-Si diode