

# Stable $\text{Al}_2\text{O}_3$ encapsulation of $\text{MoS}_2$ -FETs Enabled by CVD grown h-BN

Agata Piacentini<sup>1,2</sup>

Damiano Marian<sup>3</sup>, Daniel S. Schneider<sup>1,2</sup>, Enrique González Marín<sup>4</sup>, Zhenyu Wang<sup>5</sup>, Martin Otto<sup>1</sup>, Bárbara Canto<sup>1</sup>, Aleksandra Radenovic<sup>5</sup>, Andras Kis<sup>5</sup>, Gianluca Fiori<sup>3</sup>, Max C. Lemme<sup>1,2</sup>, and Daniel Neumaier<sup>1,6</sup>

<sup>1</sup>AMO GmbH, Otto-Blumenthal-Str. 25, 52074 Aachen, Germany

<sup>2</sup> RWTH Aachen University, Otto-Blumenthal-Straße 2, 52074 Aachen, Germany

<sup>3</sup> Università di Pisa, Via Girolamo Caruso 16, 56122 Pisa, Italy

<sup>4</sup> University of Granada, Avenida de la Fuente Nueva S/N, 18071 Granada, Spain

<sup>5</sup> École Polytechnique Fédérale de Lausanne (EPFL), Lausanne 1015, Switzerland

<sup>6</sup> Bergische Universität Wuppertal, Lise-Meitner-Straße 13, 42119 Wuppertal, Germany

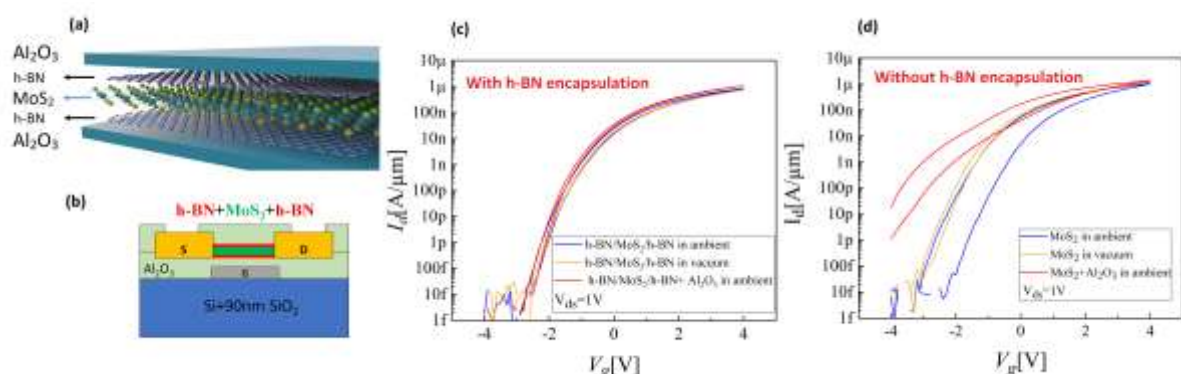
[piacentini@amo.de](mailto:piacentini@amo.de)

Two-dimensional materials have attracted massive attention especially for their potential as an alternative in ultra-scaled FET and for flexible electronic applications. Molybdenum disulfide ( $\text{MoS}_2$ ) is the most widely studied transition metal dichalcogenides because of its high carrier mobility compared to ultra-thin silicon FETs and its specific optoelectronic properties, but the electrical performance is strongly affected by the environment and dielectric interfaces, often leading to large hysteresis in  $\text{MoS}_2$ -based devices [1]. Encapsulation layer like aluminium oxide ( $\text{Al}_2\text{O}_3$ ) is widely used in (opto)-electronics. At the same time, it leads to detrimental charge transfer n-doping to  $\text{MoS}_2$  [2]. Here, we report a scalable encapsulation approach for  $\text{MoS}_2$  FETs where hexagonal boron nitride (h-BN) monolayers are employed as a barrier layer in-between each of the  $\text{Al}_2\text{O}_3$  and  $\text{MoS}_2$  interfaces (Fig. 1a and b). These devices exhibit a significant reduction of charge transfer when compared to structures without h-BN (Fig. 1c and d). This has been confirmed by ab-initio Density Functional Theory calculations. In addition, the devices with h-BN layers show very low hysteresis even under ambient operating conditions [3].

## References

- [1] Late, Dattatray J., et al. ACS nano 6.6 (2012): 5635-5641.
- [2] Na, Junhong, et al. Nanoscale 6.1 (2014): 433-441.
- [3] Piacentini, Agata et al. Unpublished manuscript (2022).

## Figures



**Figure 1:** (a) Schematic of the channel material stack and (b) cross sections of the used FET structure. Electrical characterisation of a FET (c) with h-BN encapsulation and (d) without h-BN encapsulation in different conditions.

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