Nonlinear analysis of the rectifying performance of zero-bias MoS₂-FETs for energy harvesting

A. Godoy^{1,*}, A. Medina-Rull¹, F. Pasadas¹, E. Reato², P. Palacios³, M.C. Pardo¹, F. Ruiz¹, E.G. Marín¹, M. Saeed³, Z. Wang², D. Neumaier², R. Negra³ and M.C. Lemme²

¹Departamento de Electrónica y Tecnología de Computadores, PEARL Laboratory, Facultad de Ciencias, Universidad de Granada, 18071, Granada, Spain.

²AMO GmbH, Otto-Blumenthal-Strasse 25, 52074 Aachen, Germany.

³Chair of High Frequency Electronics, RWTH University, Kopernikusstraße 16, 52074 Aachen, Germany. *agodoy@ugr.es

The main purpose of wireless energy harvesting is converting the far-field radiofrequency (RF) energy into direct current (DC) energy, able to power electronic devices, by using a rectenna. The central element that enables this conversion is the rectifier and the choice of such nonlinear device is critical as it is the central component that limits the overall efficiency and operation frequency [1]. In this work, we analyse the rectifying performance of zero-bias MoS₂ field-effect transistors (FETs) as power detectors. In doing so, we have fabricated MoS₂-FETs on wafer-scale flexible substrates (Figure 1) [2] and calibrated our physics-based large-signal model of 2D-semiconductor FETs [3] with DC (Figure 1b) and RF (Figure 1c) measurements. We have incorporated our technology computer-aided design (TCAD) tool in a commercial circuit simulator to explore and optimize the rectifying performance in terms of maximum current responsivity and cut-off frequency. In order to assess the feasibility of our MoS₂-FETs as rectifiers for future flexible rectennas targeting energy harvesting purposes, different practical ambient/dedicated RF sources have been considered and the results have been put into context against the state of the art.

References

- [1] X. Gu, S. Hemour, and K. Wu, "Far-Field Wireless Power Harvesting: Nonlinear Modeling, Rectenna Design, and Emerging Applications," *Proc. IEEE*, vol. 110(1), pp. 56–73, 2022.
- [2] E. Reato, P. Palacios, B. Uzlu, M. Saeed, A. Grundmann, Z. Wang, D. S. Schneider, Z. Wang, M. Heuken, H. Kalisch, A. Vescan, A. Radenovic, A. Kis, D. Neumaier, R. Negra, and M. C. Lemme, "Zero Bias Power Detector Circuits based on MoS₂ Field Effect Transistors on Wafer-Scale Flexible Substrates," Adv. Mater., p. 2108469, Jan. 2022.
- [3] F. Pasadas, E. G. Marin, A. Toral-Lopez, F. G. Ruiz, A. Godoy, S. Park, D. Akinwande, and D. Jiménez, "Large-signal model of 2DFETs: Compact modeling of terminal charges and intrinsic capacitances," *npj* 2D Mater. Appl., vol. 3, no. 47, pp. 1–7, 2019.

Figures



Figure 1: a) Optical micrograph of the fabricated MoS₂-FET. The MoS₂ channel is 6 µm long and 60 µm width and the scale bar is 16 µm long. Measurements (symbols) and simulations (solid lines) of **b)** the transfer characteristics and **c)** the RF performance at a bias point $V_{GS} = 4V$; $V_{DS} = 6V$. The cut-off (f_T) and the maximum oscillation (f_{max}) frequencies are highlighted in red.