Ambient Temperature Graphene Paste for Large-Area Carbon Perovskite Solar Cells: Towards Wafer-Like Concepts

Paolo Mariani¹,

Leyla Najafi^{2,3} Gabriele Bianca³, Marilena Isabella Zappia^{2,4}, Luca Gabatel³, Antonio Agresti¹, Sara Pescetelli¹, Aldo Di Carlo¹, Sebastiano Bellani^{2,3} and Francesco Bonaccorso^{2,3}

1 CHOSE Centre for Hybird and Organic Solar Energy, Università di Roma Tor Vergata, 00133 Rome, Italy

2 BeDimensional S.p.A., 16163 Genova, Italy

3 Graphene Labs, Istituto Italiano di Tecnologia, 16163 Genova, Italy; Dipartimento di Chimica e Chimica Industriale, Università degli Studi di Genova, 16146 Genova, Italy

4 Department of Physics, University of Calabria, 87036 Rende, Cosenza, Italy

paolo.mariani@uniroma2.it

Perovskite Solar Cells (PSCs) have recently shown a power conversion efficiency (PCE) exceeding 25% [1]. In order to enter the market, it is mandatory to upscale the PSC technology to large-area substrates (> 100 cm²) by means of industrial manufacturing methods. In this context, PSCs using carbon-based counter electrodes (C-CEs), namely carbon perovskite solar cells (C-PSCs), promise to provide a viable cost-effective device configuration while mitigating instability issues, such as gold ion migration toward either the hole-transporting layer (HTL) or the perovskite [2] in the conventional PSCs. In this work, we propose efficient printable C-PSCs fabricated by depositing a low-temperature-processed graphene-based carbon paste atop prototypical mesoscopic and planar n-i-p structures. Small-area (0.09 cm²) mesoscopic C-PSCs reach a PCE of 15.81% [3]. The proposed graphene-based C-CEs can be applied to large-area (1 cm²) mesoscopic devices and lowtemperature-processed planar n-i-p devices, exhibiting PCEs of 13.85 and 14.06%, respectively [3]. These PCE values are amongst the highest reported for large-area C-PSCs in the absence of back-contact metallization or additional stacked conductive components or a thermally evaporated barrier layer between the charge-transporting layer and the C-CE. In order to fabricate large-area devices, the addition of metal arids allows the PCE of wafer-like area solar cells to get closer to that of small-area devices [4]. From this perspective, we present a proof-of-concept of metallized miniwafer-like area C-PSCs (substrate area = 6.76 cm^2 , aperture area = 4.00 cm²) that achieve a PCE on active area of 13.86% and a recordhigh PCE on aperture area of 12.10% [3]. First, this result proves the metallization compatibility with our C-PSCs for the realization of large-area PSCs. Secondly, monolithic wafer-like area C-PSCs represent feasible all-solution-processed configurations that are more reliable than prototypical perovskite solar modules based on the serial connection of subcells [5]. In fact, their fabrication does not involve laser ablation-based patterning techniques for subcell separation. Meanwhile, monolithic wafer-like PSCs intrinsically mitigate hysteresis-induced performance losses and hot-spot-induced irreversible material damage caused by reverse biases in serial perovskite solar module configurations

References

- [1] Jeong J. et al., Nature, 2021 592, 381–385.
- [2] Domanski, K. et al., ACS Nano, 2016, 10 (6), 306-6314.
- [3] Mariani P. et al., ACS Applied Materials & Interfaces, 2021, DOI: 10.1021/acsami.1c02626.
- [4] Wilkinson, B. et al., Prog. Photovolt. Res Appl., 2018, 26, 659–674.
- [5] Agresti A, et al., ACS Energy Lett., 2019, 4 (8), 1862–1871.

Figures



Figure 1 (left): schematic of wafer-like PSC; (right) J-V characteristic of the proof-of-concept miniwafer PSC.