# Interface and substrate engineering in graphene/silicon Schottky junction photodiodes

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Graphene/silicon (Gr/Si) Schottky junction photodetectors (PDs) provide an efficient platform for light detection hvbrid applications [1]. High optical transmittance enables high-responsivity of graphene photodetection comparable to commercial Si photodiodes in the visible range. Also, even though limited (~2.3 %), broadband optical absorption in graphene enables extension of the spectral responsivity into fibre-optic telecommunication bands beyond the wavelength range of Si photodiodes [2].

Here we demonstrate further improvements in the figures of merit in Gr/Si PDs through substrate and interface engineering. Employment of a thin (~2 nm) native oxide layer at the Gr/Si interface as a tunneling barrier improves photovoltage responsivity as well as photon detectivities in such devices by decreasing the leakage current by an order of magnitude [3].

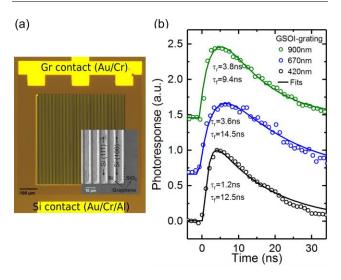
Response speeds of Gr-Si PDs fabricated on bulk silicon Si substrate (~0.5 mm) show strong wavelength dependence due to the depth-dependent photogeneration of charge carriers in the substrate. The response speed in Gr/Si PDs lags behind commercial Si photodiodes especially for

longer wavelengths ( $\lambda$  > 500nm) due to dominating contribution of speed limiting diffusion currents to the overall We photocurrent. report а significant improve in the response speed approaching cut-off frequencies of  $f_c \sim 1$  GHz in Gr/Si junctions fabricated on silicon-on-insulator (SOI) substrate with a photoactive Si thickness of 10 µm and less [4]. Furthermore, we demonstrate the control of spectral responsivity and angular dependence in Gr/Si PDs by integration of micro-optical elements through surface patterning [4].

### References

- [1] Di Bartolomeo A., Physics Reports, 606 (2016) 1-58
- [2] Riazimehr, S. et al., Solid-Sate Electronics, 115 (2016) 207-212
- [3] Selvi, H. et al., Nanoscale, 7 (2018) 3399-3409
- [4] Selvi, H. et al., Nanoscale, 40 (2018) 18926-18935

### Figures



**Figure 1:** a) Optical image of the Gr/Si Schottky junction PD fabricated on an SOI substrate. The inset shows the scanning electron microscope (SEM) image of the grating structure formed by V-shaped grooves.

b) Time-resolved photoresponse of the device under illumination at various wavelengths.